

**Amendments to the Specification:**

Please replace paragraph [36] with the following amended paragraph:

[36] In addition, the AAL2 processor 242 included in the MSC interface 240 is preferably identical to that of AAL2 processor 212, included in the BS interface 210. Similarly, the structure of AAL2 processor 212, of the BS interface 210, is preferably identical to the AAL2 processor 122, of the BSC interface 120. However the function of the AAL2 processor ~~121~~212 is opposite to that of the AAL2 processor ~~120~~122. This is explained in detail later in this section.

Please replace paragraph [37] with the following amended paragraph:

[37] FIG. 2 illustrates the structure of each ~~AAL~~AAL2 processor (122, 212, and 242) included in the mobile communication system, shown in FIG. 1. Each AAL processor includes an AAL2 transmitter (123 or 213), an AAL2 receiver (124 or 214), an internal AAL transmitter (125 or 215), and an internal AAL receiver (126 or 216).

Please replace paragraph [45] with the following amended paragraph:

[45] When the internal AAL receiver 126, of ~~AAL~~AAL2 processor 122, receives one or more internal AAL cells, generated by the internal AAL transmitter 111, the internal AAL receiver 126 restores the original user data set by demultiplexing the N internal AAL packets included in the internal AAL cells (S13). Thereafter, the internal AAL receiver 126 sends the

restored original user data set to the AAL2 transmitter 123, which is also included in the ~~AAL~~ AAL2 processor 122. Then the AAL2 transmitter initially generates M Common Part Sublayer (CPS) packets (hereinafter, "CPS packets"), by adding a CPS packet header to a  $j^{\text{th}}$  data subset of the restored data set, for  $j = 1, 2, 3, \dots, M$  (S14). Subsequently, the AAL2 transmitter 123 generates one or more AAL2-type ATM cells (hereinafter, "AL2 cells") by multiplexing the M CPS packets (S15). Finally, it sends the AAL2 cells to the BSC 200 through the E1 line (S16).

Please replace paragraph [46] with the following amended paragraph:

[46] Thereafter, when the AAL2 receiver 214, included in ~~AAL~~ AAL2 processor 212, receives the AAL2 cells containing the M CPS packets, corresponding to the original user data set (S21), it restores the original user data set by demultiplexing the CPS packets (S22). Subsequently, the internal AAL transmitter 215, of ~~AAL~~ AAL2 processor 212, generates P internal AAL packets by adding an internal AAL packet header to a  $k^{\text{th}}$  data subset of the restored original data set, for  $k = 1, 2, 3, \dots, P$  (S23). Next, the AAL transmitter 215 generates one or more internal AAL cells by multiplexing the P internal AAL packets (S24). Thereafter, the switch 220 receives the internal AAL cells and sends them to the internal AAL receiver 232, of the selector 230. Similarly, when the internal AAL receiver 232 of the selector 230 receives one or more internal AAL cells from the switch 220, it restores the original user data set by

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demultiplexing the internal AAL packets, included in the internal AAL cells (S25), and sends the restored user data to the CPU 233.